

[Web](#) [Images](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)
[Sign in](#)


[Advanced Search Preferences](#)
[Web](#)

 Results 1 - 30 of about 5,080 for [data packing +"barrel shifter"](#). (0.19 seconds)

[Variable length coder and data packing circuit - Patent 5321398](#)

4914675, Apparatus for efficiently packing data in a buffer, April, 1990 The data stored in the register in the barrel shifter 33 is then divided into ...

[www.freepatentsonline.com/5321398.html](#) ~ [Similar pages](#)

by Y Ikeda - 1994 - Cited by 16 - Related articles - All 3 versions

[Barrel shifter - Patent 5652718](#)

A barrel shifter performs multi-bit shift and rotate operations on data of ... 4583199, Apparatus for aligning and packing a first operand into a second ...

[www.freepatentsonline.com/5652718.html](#) ~ [Similar pages](#)

by TWS Thomson - 1997 - Cited by 8 - Related articles - All 7 versions

[More results from www.freepatentsonline.com](#) »

Sponsored Links

[ULINE - Shipping Tubes](#)

Huge catalog! Over 15,000 products. Same day shipping from 6 locations. [www.ULINE.com](#)

[Drum & Pail Liquid Filler](#)

High accuracy, net weight systems. Pail denesters, lidders & sealers. [DataScale.com](#)

[Shifters: In Stock Now](#)

Hurst, Long, Summit, B&M, TCI, Lokar, Turbo Action shifters [www.SummitRacing.com/Shifters](#)

[Letter A Universal Shifter with Packed Data Formats 1 ...](#)

Keywords: Universal shifter, Barrel shifter, SIMD, Packed data.,

Crossbar network ... to execute data reorganization operations such as pack, ...

[linkinghub.elsevier.com/retrieve/pii/S1434841104701855](#) -

[Similar pages](#)

by WK Jeong - 2003 - Related articles - All 3 versions

[ScienceDirect - Nuclear Instruments and Methods in Physics ...](#)

The 2D-Compressor generates data that have to be temporarily stored in a FIFO buffer. This is done by means of a barrel shifter that outputs a fixed length ...

[linkinghub.elsevier.com/retrieve/pii/S016890020401040X](#) ~ [Similar pages](#)

by S Antinori - 2004 - Cited by 3 - Related articles - All 4 versions

[More results from linkinghub.elsevier.com](#) »

[Variable length coder and data packing circuit - Patent # 5321398 ...](#)

5321398, Variable length coder and data packing circuit The data stored in the register in the barrel shifter 33 is then divided into high- and ...

[www.patentgenius.com/patent/5321398.html](#) - 45k - Cached - [Similar pages](#)

[Apparatus for packing parallel data words having a variable width ...](#)

An apparatus for packing data, receiving parallel data words arranged in blocks of data, As the bit shifter 30, for example a 32-bit barrel shifter, ...

[www.patentgenius.com/patent/4963867.html](#) - 74k - Cached - [Similar pages](#)

[More results from www.patentgenius.com](#) »

[Circuit and method of bit-packing and bit-unpacking using a barrel ...](#)

Circuit and method of bit-packing and bit-unpacking using a barrel shifter - US Patent

5499382 from Patent Storm. A data compressor generates codewords ...

[www.patentstorm.us/patents/5499382.html](#) ~ [Similar pages](#)

by JA Pasco-Anderson - 1996 - Cited by 4 - Related articles - All 3 versions

Variable length coder and data packing circuit - US Patent 5321398

The data stored in the register in the barrel shifter 33 is then divided Inventor:

Bahgat4914675, Apparatus for efficiently packing data in a buffer ...

www.patentstorm.us/patents/5321398/description.html - [Similar pages](#)

[More results from www.patentstorm.us »](#)

Google Answers: What is a "barrel shift"?

Yes, a barrel shifter can be used to rotate data. ... 32-bit cascadable barrel shifter "designed for use in floating-point normalization, word pack/unpack, ...

answers.google.com/answers/threadview?id=388350 - 18k - [Cached](#) - [Similar pages](#)

A VME barrel shifter system for event reconstruction for up to 3 ...

of this barrel shifter system are switch modules, optical to ... data flow and the data set is arranged in. a. set of pack- ets of the same length. ...

ieeexplore.ieee.org/iel1/23/6500/00256625.pdf - [Similar pages](#)

by O Sasaki - 1993 - Cited by 2 - Related articles - All 6 versions

Timing-Driven Decomposition of a Fast Barrel Shifter

length coding, word packing/unpacking, bit indexing, address ... a barrel shifter, if the data input signal is n-bits wide, then the ...

ieeexplore.ieee.org/iel5/4483493/4488527/04488648.pdf - [Similar pages](#)

[More results from ieeexplore.ieee.org »](#)

TechOnline | New DSP Architectures Go "Post-Harvard" for Higher ...

Often this is accomplished with data packing, which allows registers, data paths, ... a 40-bit barrel shifter, and quad 8-bit ALUs for video operations. ...

www.dspdesignline.com/showArticle.jhtml?articleID=192200554 - 59k - [Cached](#) - [Similar pages](#)

Inventors Yuji Ishikawa, Yokohama (JP) Assignees Canon Kabushiki ...

Control is performed to byte pack the bits of the last byte and the shifted bits outputted from the barrel shifter, and coded data is converted into data in ...

www.patentlens.net/patentlens/search_ajax.cgi?patnum=US/5146220 - 19k -

[Cached](#) - [Similar pages](#)

C5000™ Low Power DSPs - TMS320C54x DSPs - TMS320VC5404 - TI.com

Samples; Pricing/Packaging; Technical Documents. Features; Related Products; Tools & Software. Inventory; Symbols/Footprints; Quality & Pb-Free Data ...

focus.ti.com/docs/prod/folders/print/lms320vc5404.html - 60k - [Cached](#) - [Similar pages](#)

AES E-Library: A Single-Chip DSP Implementation of a High-Quality ...

These include an optimized FFT kernel, provision for low-overhead block floating-point, and a barrel shifter for data packing and envelope information ...

www.aes.org/e-lib/browse.cfm?elib=6458 - [Similar pages](#)

by S Vernon - Cited by 4 - Related articles

Stratix II FPGA Design Building Block Performance

A barrel shifter rotates the input data bits by the amount specified by the resources usage by packing logic functions and common inputs together. ...

www.altera.com/products/devices/stratix-fpgas/stratix-ii/stratix-ii/features/architecture/st2-dbb_perf.html - 53k - [Cached](#) - [Similar pages](#)

Details and datasheet on part: DSP56303PV100

Program Address Generator 24 × 24 + 56 56-bit MAC Two 56-bit Accumulators 56-bit Barrel Shifter Data ALU Power Management JTAG OnCETM ...

www.digchip.com/datasheets/parts/datasheet/311/DSP56303PV100.php - 23k - [Cached](#) - [Similar pages](#)

Design and Implementation of a High-Performance Microprocessor ...

The decompressor then shifts in more compressed data using a barrel shifter and concatenates them with the remaining compressed bits. ...
doi.ieeecomputersociety.org/10.1109/DCC.2008.90 - [Similar pages](#)
by X Chen - [Related articles](#) - [All 2 versions](#)

[PDF] [DSP56311 Technical Data](#)

File Format: PDF/Adobe Acrobat
Data arithmetic logic unit (Data ALU) with fully pipelined 24×24 -bit parallel Multiplier-Accumulator (MAC),, 56-bit parallel barrel shifter (fast shift ...
www.freescale.com/files/dsp/doc/data_sheet/DSP56311.pdf - [Similar pages](#)

[EP405805 Ibm european software patent - Apparatus for processing ...](#)

Data packing of the line buffer (i.e. the RAM) can be performed on a multiple ... Figures 20 - 22 illustrate the modules constituting the barrel shifter; ...
gauss.ffii.org/PatentView/EP405805 - 73k - [Cached](#) - [Similar pages](#)
by S Parent

[PDF] [A Study of the Speedups and Competitiveness of FPGA Soft Processor ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)
offer excellent packaging and communication advantages, a soft- a barrel shifter to enable the bs and bsi instructions. ...
www.cs.ucr.edu/~vahid/pubs/date05_warp_microblaze.pdf - [Similar pages](#)
by R Lysecky - 2005 - [Cited by 35](#) - [Related articles](#)

[Dynamically-reconfigurable Elemental Computing Arrays \(ECAs ...](#)

Nov 7, 2007 ... This enables shifting, interleaving, packing, and unpacking operations ... BSHF: Barrel SHIFter. This enables shifting operations and can be ...
www.pldesignline.com/howto/202803397 - 56k - [Cached](#) - [Similar pages](#)

[\(WO/1999/024942\) A METHOD OF LABELLING AN ARTICLE](#)

At a data packing density of 1.3 bits per mm, this implies that over 53 mm a ring or rotating barrel shifter, and rotate them by 35 bits (in steps of ...
www.wipo.int/pctdb/en/wo.jsp?IA=WO1999024942&DISPLAY=DESC - 42k - [Cached](#) - [Similar pages](#)

[PDF] [XPP Technologies XPP-III Processor Overview](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)
XPP (eXtreme Processing Platform) is a data processing architecture based on a hier-
BREG: barrel shifter, packing, unpacking and clipping operators ...
www.pactxpp.com/main/download/XPP-III_overview_WP.pdf - [Similar pages](#)

[PDF] [*EP000697649B1*](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)
typically utilize barrel shifter circuitry to allow a plurality of bytes to be shifted by reordered and shifted to perform data packing and unpacking. ...
<https://publications.european-patent-office.org/PublicationServer/documentpdf.jsp?idocid=4839082&iebug=.pdf> - [Similar pages](#)

[Texas Instruments' TMS320C40](#)

20-Mbytes/sec asynchronous transfer rate at each port for maximum data ... Single-cycle barrel shifter for 0-31 single-cycle right or left shifts for fast ...
osl-www.cs.umass.edu/IUA/TMS320C40.html - 4k - [Cached](#) - [Similar pages](#)

[ARM1136JF-S™ and ARM1136J-S™ Technical Reference Manual 1.2.1. Core](#)

Only load, store, and swap instructions can access data from memory. ... The Shift stage contains the full barrel shifter. All shifts, including those ...
infocenter.arm.com/help/topic/com.arm.doc.ddi0211j/Cacebjhf.html - 15k - [Cached](#) - [Similar pages](#)

[PDF] [Xilinx XAPP512 Implementing Keypad Scanners with CoolRunner-II ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Introduction. As handheld devices such as cell phones pack more and more features into them, they require more effective ways of entering data.

www.xilinx.com/support/documentation/application_notes/xapp512.pdf - [Similar pages](#)

[FLASH] [Loading: Registers Data Memory Program Memory Instruction Set ...](#)

File Format: Shockwave Flash

The barrel shifter can shift data up to 16 bits left or right in a single packaging and electrical characteristics, the device datasheets listed here ...

dkc1.digikey.com/us/en/tds/Microchip/dsPIC30F_Architecture_Part1_NoAudio/dsPIC30F_Architecture_Part1_NoAudio.swf - [Similar pages](#)

BYTE.com

This capability allows programmers to tightly pack data or code and conserve RAM, which is a high-cost item for embedded applications.

www.byte.com/art/9608/sec5/art3.htm - 32k - [Cached](#) - [Similar pages](#)

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) | [Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)

[Learn more](#)